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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/816,738

04/02/2004

Mark G. Barmettler

4100-00200

1386

30652

7590

07/31/2008

CONLEY ROSE, P.C.

5601 GRANITE PARKWAY, SUITE 750

PLANO, TX 75024

EXAMINER

PHAM, BRENDA H

ART UNIT

PAPER NUMBER

2616

MAIL DATE

DELIVERY MODE

07/31/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/816,738	Applicant(s) BARMETTLER, MARK G.	
	Examiner BRENDA PHAM	Art Unit 2616	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 20 May 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) 13-28 is/are withdrawn from consideration.
- 5) ☐ Claim(s) 6-12 is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☐ Claim(s) 5 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>08/21/07;07/01/04</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims 1-28 are pending in the application. Non-elected claims 13-28 are withdrawn from consideration.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Andrews et al (US 5,572,695).

Regarding claim 1, Andrews et al disclose an adapter channel mapping system for mapping channels of a network, comprising:

a memory component operable to communicate with the network and having a first memory portion (56 Lookup/Buffer area) and a second memory portion (56 Lookup/Buffer Area), the first memory portion operable to communicate with a first physical channel of the network and the second memory portion operable to communicate with a second physical channel of the network (see figure 6); and
a mapping component operable to map, based on a first map, to the first and second memory portions based on a first configuration of the network and further operable to map, based on a second map, to the first and second memory portions based on a second configuration of the network (**see figure 1, 3, 4, 6 and 13, “The first and**

second mapping units are operative to receive (i) first and second logical addresses generated by the first and second digital processors respectively and (ii) first and second address mapping information respectively, and generate first and second physical addresses such that each of the digital processors can independently access any of a plurality of memory locations within the data storage unit.” see abstract.)

Regarding claims 2 and 3, a selector component in communication with the mapping component and operable in a first selection mode to cause the mapping component to map based on the first map and operable in a second selection mode to map based on the second map. ("Based on the inputs from register 36 and CAB1, TMML 38 will map the logical DSP addresses 52 of DSP 34 into the physical DRAM memory locations according to the maps shown in FIG 4 and 5 for modes 1, 2 and 3 respectively. Similarly, based on the inputs from register 44 and CAB2, TMML 46 will map the logical DSP addresses 52 of DSP 42 into the physical DRAM memory locations according to the maps shown in FIG. 4 and 5 for modes 1, 2 and 3, respectively.) Col. 10, lines 20-30).

Regarding claim 4, wherein the selector component is further defined as a switch in communication with the mapping component, the switch having a first switch position associated with the first selection mode and a second switch position associated with the second selection mode (**"multiplexer 40 connects the individual conductors in**

41 and CDB2 to the corresponding individual conductors in the storage bus 50. This switching back and forth between busses 39, CDB1 and 41 CDB2 is repeated over and over again in a continuing manner to storage bus 50.") (see figure 3, element 40)

Allowable Subject Matter

4. Claims 6-12 are allowed over prior arts made of record.
5. Claim 5 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brenda Pham whose telephone number is (571) 272-3135. The examiner can normally be reached on Monday-Friday from 9:00 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynn D. Feild, can be reached on (571) 272-2092.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-2600.

July 30, 2008

/Brenda Pham/

Primary Examiner, Art Unit 2616

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